

Arasan Chip Systems Announces 64-bit compatible Interface IP for Mobile Storage

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Arasan's Total IP Solution for 64-bit processors, such as the ARMv8, provides support for SD, eMMC, ONFI and UFS mobile storage controllers in 32-bit or 64-bit bus configurations.

San Jose, California – December 10, 2013 – Arasan Chip Systems, Inc. (“Arasan”), a leading provider of Total IP Solutions for mobile applications, announced today the availability of compatible mobile storage interfaces for 64-bit processors, using system host interfaces AHB, AXI or OCP. Compatible standards include: SD 3.0, SD 4.0, SDIO 3.0, SDIO 4.0, eMMC 4.51, eMMC 5.0, ONFI 3.2, and UFS 2.0.

The mobile processing world is following the roadmap set by personal computing in the last decade by adopting 64-bit processors. In the last few months, many of the leading mobile application processor suppliers have announced ARMv8 architecture licenses and/or ARM Cortex A50 series licenses. Apple's A7 processor embodied in the iPhone 5s and certain iPad models, was launched in September. Several major IC suppliers including Huawei, LG, MediaTek, nVidia, Qualcomm and Samsung, have announced development of future mobile products based on the ARMv8.

The Arasan Mobile Storage Host Controllers – SD, SDIO, eMMC, ONFI and UFS – now support 32-bit and 64-bit AHB or AXI bus from ARM or the OCP (Open Core Protocol). Bus configuration is supported in both digital IP the Arasan software stacks. Validation support will be provided with Arasan's HVP (Hardware Validation Platform) for these controllers.

“Arasan is the market leader in analog and digital IP for mobile storage. Providing compatibility for the next generation of application processor demonstrates Arasan's continued leadership role for interface standards from JEDEC, SD Association and ONFI”, said Andrew Haines, VP of Marketing.

Availability

Digital IP for Arasan's **SD / eMMC, ONFI**, and **UFS** host controllers is available now. Analog IP (**PHY**) including optimized Pads, for these controllers is also available.

About Arasan

Arasan Chip Systems is a leading provider of Total IP Solutions for mobile storage and connectivity applications. Arasan's high-quality, silicon-proven, Total IP Solutions include digital IP cores, analog PHY interfaces, verification IP, hardware verification kits, protocol analyzers, software stacks and drivers, and optional customization services for MIPI, USB, UFS, SD, SDIO, MMC/eMMC, UFS, and many other popular standards. Arasan's Total IP products serve system architects and chip design teams in mobile, gaming and desktop computing systems that require silicon-proven, validated IP, delivered with the ability to integrate and verify both digital, analog and software components in the shortest possible time with the lowest risk.

Unlike many other IP providers, Arasan's Total IP Solution encompasses all aspects of IP development and integration, including analog and digital cores, hardware development kits, protocol analyzers, validation IP and software stacks and drivers and optional architecture consulting and customization services. Based in San Jose, CA, USA, Arasan Chip Systems has a 17 year track record of IP and IP standards development leadership.

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