

Arasan Chip Systems Announces Successful InterOp testing of SD 4.1 and UHS-II

Arasan's SD 4.1 host, software stack and UHS-II interface board were tested with SD 4 cards from leading industry suppliers at the SDA InterOP, Tokyo, November 27, 2014.

San Jose, California – Dec 4, 2014 - Arasan Chip Systems, Inc. ("Arasan"), a leading provider of Total IP Solutions for mobile applications, announced results of successful compatibility testing of Arasan's SD 4.1 Host interface, software stack and UHS-II interface card at the SDA InterOp in Tokyo, November 2014. Leading device suppliers with SD solutions spanning 8GB to 64GB were successfully tested in a Linux based host environment with Arasan digital and analog IP and software stack.

To meet the ever increasing data transfer rate in high end applications, such as professional broadcasting transmission or advanced high resolution display, the SD 4.1 specification calls out the maximum performance of 1.56 Gbps at UHS-II full duplex mode per lane or half duplex UHS-II at 3.16 Gbps. With ADMA 3, the OS driver is now able to issue multiple read or multiple write commands at once, without having to wait for the SD controller to complete one command at a time. Once the SD host controller has collected multiple commands, it will then manage and complete them without intervention from the host software driver. Thus, the UHS-II 1.56 Gbps interface can be more effectively utilized and maximize the system throughput. This feature can be very useful when running multithreaded applications where multiple applications are constantly updating their status or swapping their contents by writing or reading small chunk of data to or from the memory card.

Arasan introduced the industry's first combined SD 4.1, SDIO 4.1, eMMC 4.5.1 Host Controller in early 2014. Arasan's long-standing active contributor status to both SDA and JEDEC allows a first to market advantage with both storage card and embedded Flash memory controllers for mobile applications.

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Helping engineers to accelerate time to market, Arasan provides a complete suite of tools for IP integration including SD 4.1 link layer controller IP, UHS-II PHY in advanced process technologies, verification IP with robust test suite, FPGA validation and development platform, and software stack in source code. The link layer controller IP is designed with the most interoperability in place, based on Arasan's extensive experience in working with many SD host and SD device companies. Arasan has conducted several interoperability tests to ensure wide range of compatibility with different products in the market. Developed in advanced, 40nm and below process nodes, the UHS-II PHY is designed for higher signal integrity and lower power consumption compared to competition. Arasan has optimized its Linux based SD software driver and tested its performance by running the Linux storage device benchmark; the benchmark results demonstrated the software driver achieving more than 90% bandwidth efficiency to squeeze out every bit of performance improvement.

Arasan's SD 4.1 Host is offered as an integrated eMMC 4.51 and SDIO 4.1 solution. Arasan also provides IP and software for SD 4 devices.

Availability

Arasan SD 4.1 / SDIO 4.1 / eMMC 4.5. Host Controller, SD 4.1 Device Controller and SDIO 4.1 Device Controller are available now, including digital (RTL) IP, UHS-II PHY (SD 4.1), Verification IP (VIP), Linux Software Stack in source code, and Hardware Validation Platforms (HVP), with all supporting documents.

About Arasan

[Arasan Chip Systems](#) is a leading provider of Total IP Solutions for mobile storage and connectivity applications. Arasan's high-quality, silicon-proven, Total IP Solutions include digital IP cores, analog PHY interfaces, verification IP, hardware verification kits, protocol analyzers, software stacks and drivers, and optional customization services for MIPI, USB, UFS, SD, SDIO, MMC/eMMC, UFS, and many other popular standards. Arasan's Total IP products serve system architects and chip design teams in mobile, gaming and desktop computing systems that require silicon-proven, validated IP, delivered with the ability to integrate and verify both digital, analog and software components in the shortest possible time with the lowest risk.

Unlike many other IP providers, Arasan's Total IP Solution encompasses all aspects of IP development and integration, including analog and digital cores, hardware development kits, protocol analyzers, validation IP and software stacks and drivers and optional architecture consulting and customization services. Based in San Jose, CA, USA, Arasan Chip Systems has a 17 year track record of IP and IP standards development leadership.

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